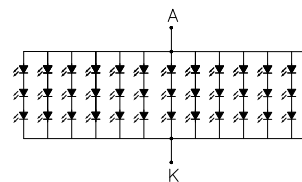


DETAIL A  
(SCALE 50:1)



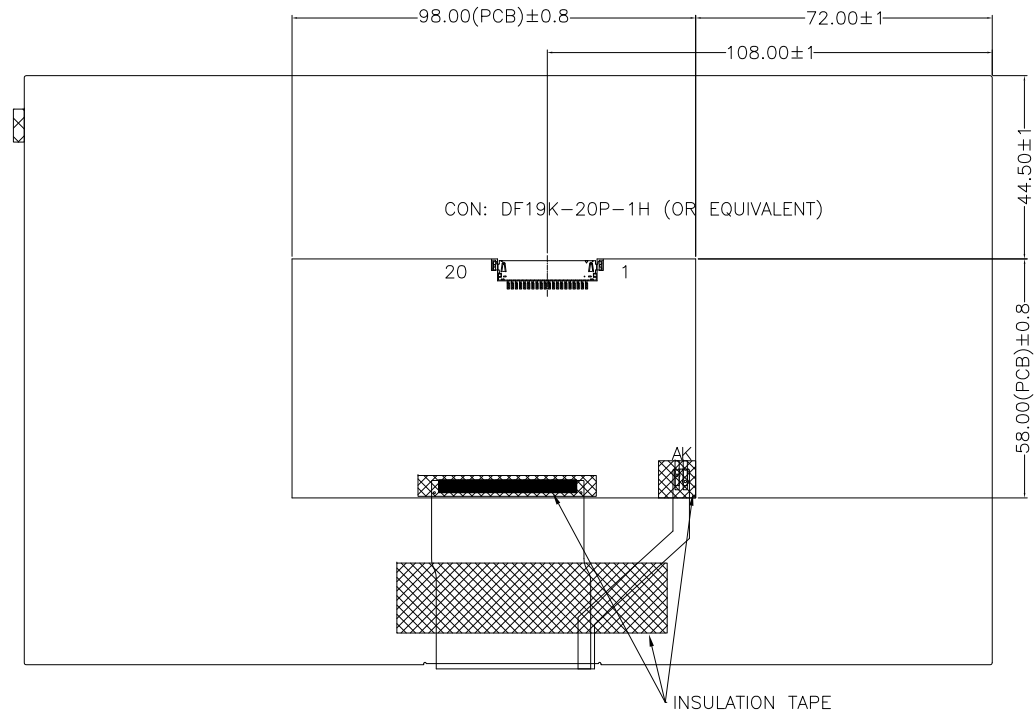
BACKLIGHT CIRCUIT DIAGRAM

| PIN ASSIGNMENT |        |
|----------------|--------|
| PIN NO.        | SYMBOL |
| 1              | VCC    |
| 2              | VCC    |
| 3              | VSS    |
| 4              | VSS    |
| 5              | RIN0-  |
| 6              | RIN0+  |
| 7              | VSS    |
| 8              | RIN1-  |
| 9              | RIN1+  |
| 10             | VSS    |
| 11             | RIN2-  |
| 12             | RIN2+  |
| 13             | VSS    |
| 14             | RCLK-  |
| 15             | RCLK+  |
| 16             | VSS    |
| 17             | VDD_5V |
| 18             | VDD_5V |
| 19             | ADJ    |
| 20             | VSS    |

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE.
2. OPERATING VOLTAGE: VDD=3.3V.
3. VIEWING DIRECTION: 12 O'CLOCK.
4. IC DRIVER: HX8282-A01+HX8696-A01.
5. OPERATING TEMP: -20°C ~ 70°C.
6. STORAGE TEMP: -30°C ~ 80°C.
7. SURFACE LUMINANCE: 550CD/M<sup>2</sup>(TYP.)
8. LED BACKLIGHT: 39-CHIP WHITE LED, If=260mA, Vf=9.6V.
9. SURFACE TREATMENT: ANTI-GLARE.
10. GENERAL TOLERANCE: ±0.3MM.
11. ROHS COMPLIANT.

|                |  |                                |             |
|----------------|--|--------------------------------|-------------|
|                |  | MULTI-INNO TECHNOLOGY CO.,LTD. |             |
| 03             | CORRECT THE DIMENSIONS BETWEEN LCD A.A AND LCM O.D.<br>CORRECT THE DIMENSIONS BETWEEN LCD V.A AND LCM O.D. | RITA                           | 2020.08.18  |
| 02             | CORRECT THE SURFACE LUMINANCE  | RITA                           | 2019.09.09  |
| 01             | FIRST ISSUE  | RITA                           | 2019.06.21  |
| VER.           | REVISED DESCRIPTION  | REVISER                        | DATE        |
| DRAWN BY: RITA |  | 2019.06.21                     |             |
| CHECKED BY:    |  | SCALE : 1/1                    |             |
| APPROVED BY:   |  | UNIT: mm                       |             |
| VERSION NO: 03 |  | SHEET NO.: 1/2                 |             |
| MODULE P/N:    |  |                                | MI101021T-1 |
| DESCRIPTION:   |  |                                | TFT MODULE  |



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|      |  |         |            |              |      |            |                |              |
|------|--|---------|------------|--------------|------|------------|----------------|--------------|
| 03   | CORRECT THE DIMENSIONS BETWEEN LCD A.A AND LCM O.D.<br>CORRECT THE DIMENSIONS BETWEEN LCD V.A AND LCM O.D. | RITA    | 2020.08.18 | DRAWN BY:    | RITA | 2019.06.21 |                | MODULE P/N:  |
| 02   | CORRECT THE SURFACE LUMINANCE  | RITA    | 2019.09.09 | CHECKED BY:  |      |            | SCALE : 1/1    | MH1010Z1T-1  |
| 01   | FIRST ISSUE  | RITA    | 2019.06.21 | APPROVED BY: |      |            | UNIT: mm       | DESCRIPTION: |
| VER. | REVISED DESCRIPTION  | REVISER | DATE       | VERSION NO:  | 03   |            | SHEET NO.: 2/2 | TFT MODULE   |